Ref #	Hits	Search Query	DBs	Default Operator	Plurals	Time Stamp
		((circuit adj board) and semiconductor and bump$ and adhesive and resin) and electro$	USPAT; JPO	OR	OFF	2001/09/17 11:55
		(((printed adj circuit adj board) and support) and rail$) and rod$	USPAT; JPO	OR	OFF	2001/01/16 07:15
L1	5	(US-3826483-$ or US-3930644-$ or US-5694843-$ or US-5707052-$ or US-5899446-$).did.	USPAT	OR	ON	2005/04/06 11:05
L2	86	(position$3 adj device) with PCB	USPAT; JPO; DERWENT	OR	ON	2005/04/06 11:06
L3	68	2 and @ad<"19990901"	USPAT; JPO; DERWENT	OR	ON	2005/04/06 11:06
L4	6	("3889936" | "4159505" | "4976415" | "5711065" | "6161811" | "6251493").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/04/06 11:08
L5	2	("4980971" | "5084959").PN.	US-PGPUB; USPAT; USOCR	OR	ON	2005/04/06 11:11
S1	22614	"269"/$.ccls.	USPAT	OR	OFF	2001/09/18 11:37
S2	7	"269"/$.ccls. and (printed adj circuit adj board adj support)	USPAT; JPO	OR	OFF	2001/01/16 06:01
S3	37	(printed adj circuit adj board adj support) and rail$	USPAT; JPO	OR	OFF	2001/01/16 06:04
S4	293	printed adj circuit adj board adj support	USPAT; JPO	OR	OFF	2001/01/16 07:12
S5	62275	printed adj circuit adj board	USPAT; JPO	OR	OFF	2004/05/11 14:19
S6	20034	(printed adj circuit adj board) and support	USPAT; JPO	OR	OFF	2001/01/16 07:13
S7	2274	((printed adj circuit adj board) and support) and rail$	USPAT; JPO	OR	OFF	2001/01/16 07:14
S8	479	((((printed adj circuit adj board) and support) and rail$) and rod$) and (block or table)	USPAT; JPO	OR	OFF	2001/01/16 08:14
S9	53	peckham.in.	USPAT	OR	OFF	2001/01/16 07:42
S10	7	peckham.in. and mark	USPAT	OR	OFF	2001/01/16 07:41
S11	0	teitenberg.in.	USPAT	OR	OFF	2001/01/16 07:41
S12	0	meridian.in.	USPAT	OR	OFF	2001/01/16 07:42
S13	1686	tony.in.	USPAT	OR	OFF	2001/01/16 07:42
S14	0	tony.in. and teitenberg	USPAT	OR	OFF	2001/01/16 07:42
S15	0	peckham.in. and (printed adj circuit adj board adj support)	USPAT	OR	OFF	2001/01/16 07:42

S16	0	tony.in. and (printed adj circuit adj board adj support)	USPAT	OR	OFF	2001/01/16 07:43
S17	233	269/903.ccls. and support	USPAT	OR	OFF	2001/01/16 08:25
S18	80	(269/903.ccls. and support) and (circuit adj board)	USPAT	OR	OFF	2001/01/16 08:25
S19	875	(circuit adj board) and semiconductor and bump$ and adhesive and resin	USPAT; JPO	OR	OFF	2001/09/18 15:16
S20	3	"6034437".URPN.	USPAT	OR	OFF	2001/09/17 11:17
S21	3	"6034437".URPN.	USPAT	OR	OFF	2001/09/17 11:19
S22	8	("5352926" | "5523622" | "5703405" | "5786271" | "5834844" | "5844304" | "5886409" | "5886415").PN.	USPAT	OR	OFF	2001/09/17 11:19
S23	1	("6274389").PN.	USPAT; USOCR	OR	OFF	2004/05/11 14:19
S24	0	"6274389".URPN.	USPAT	OR	OFF	2004/05/11 14:19
S25	22	("4159976" | "4181645" | "4866108" | "5120665" | "5137936" | "5250848" | "5274913" | "5355580" | "5371328" | "5423931" | "5512613" | "5560934" | "5567266" | "5760337" | "5783867" | "5821456" | "5872158" | "5912282" | "5914381" | "5932682" | "5973033" | "6074895").PN.	USPAT	OR	OFF	2001/09/18 11:39
S26	922	29/271,289.1,760.ccls.	USPAT	OR	OFF	2004/05/11 14:19
S27	184141	PCB or (print$2 adj circuit adj board)	USPAT; JPO; DERWENT	OR	ON	2004/05/11 14:20
S28	785	print$2 adj circuit adj board adj support	USPAT; JPO; DERWENT	OR	ON	2004/05/11 14:20
S29	785	(PCB or (print$2 adj circuit adj board)) and (print$2 adj circuit adj board adj support)	USPAT; JPO; DERWENT	OR	ON	2004/05/11 14:20
S30	3	29/271,289.1,760.ccls. and ((PCB or (print$2 adj circuit adj board)) and (print$2 adj circuit adj board adj support))	USPAT; JPO; DERWENT	OR	ON	2004/05/11 14:21
S31	1	(US-6412768-$).did.	USPAT	OR	OFF	2004/05/11 14:21
S32	1	((US-6412768-$).did.) and (contact adj surface)	USPAT; JPO; DERWENT	OR	ON	2004/05/11 15:15

S33	1	(29/271,289.1,760.ccls. .and ((PCB or (print$2 adj circuit adj board)) and (print$2 adj circuit adj board adj support))) and (contact adj surface)	USPAT; JPO; DERWENT	OR	ON	2004/05/11 14:22
S34	1	((US-6412768-$).did.) and (positioning adj surface$1)	USPAT; JPO; DERWENT	OR	ON	2004/05/11 14:25
S35	17	"3930644".URPN.	USPAT	OR	ON	2004/05/11 14:41
S36	3	"5899446".URPN.	USPAT	OR	ON	2004/05/11 14:42
S37	39	(print$2 adj circuit adj board adj support) and bias$3 and stop	USPAT; JPO; DERWENT	OR	ON	2004/05/11 15:06
S38	1	((US-6412768-$).did.) and (stop)	USPAT; JPO; DERWENT	OR	ON	2004/05/11 15:17
S39	0	((US-6412768-$).did.) and (limit$3)	USPAT; JPO; DERWENT	OR	ON	2004/05/11 15:17

IEEE Xplore®
RELEASE 2.0

Welcome United States Patent and Trademark Office

Search Results

BROWSE　　　　SEARCH　　　　IEEE XPLORE GUIDE

Results for "((printed circuit board) <and> support <and> rail<in>metadata)"

☑ e-mail

Your search matched **7** of **1142142** documents.

A maximum of **100** results are displayed, **25** to a page, sorted by **Relevance** in **Descending** order.

» View Session History

» New Search

Modify Search

» Key

| ((printed circuit board) <and> support <and> rail<in>metadata) | »» |

IEEE JNL	IEEE Journal or Magazine
IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

☐ Check to search only within this results set

Display Format:　　　● Citation　　　○ Citation & Abstract

Select　　　**Article Information**

☐　1. **Transformation of mechanized locomotives into semi-autonomous machines**
Stolarczyk, L.G.; Smoker, K.A.;
Industry Applications, IEEE Transactions on
Volume 28, Issue 4, July-Aug. 1992 Page(s):900 - 906

AbstractPlus | Full Text: PDF(700 KB)　IEEE JNL

☐　2. **Calculation of the frequency-dependent impedance of rail tracks using a four-parameter equivalent tu model**
Yaw-Juen Wang; Yue-Lin Tsai;
Power Delivery, IEEE Transactions on
Volume 19, Issue 3, July 2004 Page(s):1142 - 1147

AbstractPlus | References | Full Text: PDF(336 KB)　IEEE JNL

☐　3. **Recent advances in power electronics technology for industrial and traction machine drives**
Jahns, T.M.; Blasko, V.;
Proceedings of the IEEE
Volume 89, Issue 6, June 2001 Page(s):963 - 975

AbstractPlus | References | Full Text: PDF(220 KB)　IEEE JNL

☐　4. **Rotating coil measurement system for the APT/LEDA CCDTL quadrupole magnets**
Barlow, D.B.;
Applied Superconductivity, IEEE Transactions on
Volume 10, Issue 1, March 2000 Page(s):1447 - 1449

AbstractPlus | Full Text: PDF(96 KB)　IEEE JNL

☐　5. **Dublin Area Rapid Transit**
Waters, C.D.; Farrell, M.; Grainger, R.P.; Leahy, P.J.; Mellitt, B.;
Electric Power Applications, IEE Proceedings B [see also IEE Proceedings-Electric Power Applications]
Volume 135, Issue 3, May 1988 Page(s):134 - 150

AbstractPlus | Full Text: PDF(1432 KB)　IEE JNL

☐　6. **Using power supply current monitoring and P1149.4 for parametric testing of passive components**
Machado da Silva, J.; Silva Matos, J.;
Circuits and Systems, 1997. ISCAS '97., Proceedings of 1997 IEEE International Symposium on
Volume 4, 9-12 June 1997 Page(s):2713 - 2716 vol.4

AbstractPlus | Full Text: PDF(344 KB)　IEEE CNF

7. **Dual-mode low-power bus encoding for high-performance bus drivers**
Naehyuck Chang; Kwan-Ho Kim; Heonshik Shin;
TENCON 99. Proceedings of the IEEE Region 10 Conference
Volume 2, 15-17 Sept. 1999 Page(s):876 - 879 vol.2

AbstractPlus | Full Text: PDF(356 KB)   IEEE CNF